


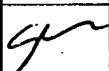
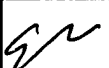

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<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>  Date Submitted: June 2, 2006  (use as many sheets as necessary)				Application Number	Unassigned <b>40/581555</b>
				Filing Date	09/15/2004
				First Named Inventor	Shinichi HORI
				Group Art Unit	Unassigned <b>2817</b>
				Examiner Name	Unassigned <b>MATOLA</b>
Sheet	1	of	1	Attorney Docket Number	040373-0389

U.S. PATENT DOCUMENTS						
Examiner Initials*	Cite No. <sup>1</sup>	U.S. Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number	Kind Code <sup>2</sup> (if known)			

UNPUBLISHED U.S. PATENT APPLICATION DOCUMENTS						
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		Serial Number	Kind Code <sup>2</sup> (if known)			

FOREIGN PATENT DOCUMENTS								
Examiner Initials*	Cite No. <sup>1</sup>	Foreign Patent Document			Name of Patentee or Applicant of Cited Documents	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T <sup>6</sup>
		Office <sup>3</sup>	Number <sup>4</sup>	Kind Code <sup>5</sup> (if known)				
<i>[Signature]</i>	A1	JP	2004-266316	A	NEC CORP.	09-24-2004		A
<i>[Signature]</i>	A2	JP	2000-269751	A	MITSUMI ELECTRIC CO.	09-29-2000		A
<i>[Signature]</i>	A3	JP	2000-134045	A	TEXAS INST JAPAN LTD.	05-12-2000		A
<i>[Signature]</i>	A4	JP	08-078973		KANEBO LTD.	03-22-1996		A
<i>[Signature]</i>	A5	JP	08-032369		MITSUMI ELECTRIC CO.	02-02-1996		A
<i>[Signature]</i>	A6	JP	06-061859		HITACHI LTD.	03-04-1994		A
<i>[Signature]</i>	A7	JP	05-283948		NEC IC MICROCOMPUT SYST LTD.	10-29-1993		A

NON PATENT LITERATURE DOCUMENTS				
Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.) date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>6</sup>	
	A8	B.I. MILLER et al., "Strain-compensated strained-layer superlattices for 1.5 μm wavelength lasers," Appl. Phys. Lett. 58(18), May 6, 1991, pp. 1952-1954, 1991 American Institute of Physics.		
	A9	B. PANKIEWICZ, et al., "A field programmable analog array for CMOS continuous-time OTA-C filter applications," IEEE Journal of Solid-state circuits, Vol. 37:2, February 2002, pp. 125-136.		
	A10	T. TSUCHIYA et al., "Large number of periods in highly strained InGaAlAs/InGaAlAs MQW structures grown by metalorganic vapor-phase epitaxy," 11 <sup>th</sup> International Conference on Indium Phosphide and Related Materials, May 16-20, 1999, pp. 47-50.		
	A11	A 1.5 V 75 dB-Dynamic Range 3 <sup>rd</sup> -Order G <sub>m</sub> -C Filter Integrated in a 0.18 μm Standard Digital CMOS Process," ESSCIRC 2002, pp. 647-650.		

Examiner Signature	<b>S J Mottola</b>	Date Considered	<b>12-10-07</b>
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				Examiner Name	Unassigned MOTTOLA
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4	A12	C.P. SELTZER, et al., "Zero-net-strain multiquantum well lasers," Electronics Letters, Vol. 27:14, July 4, 1991, pp. 1268-1270.	

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